

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE
PATENT

Inventor(s): Jung-Chien Chang Docket No.: 39524.0069
Serial No.: 10/615,139 Filing Date: July 9, 2003
Art Unit: 1762 Examiner: James Lin
TITLE: A METHOD FOR PREPARING Confirmation No.:
THIN INTEGRATED CIRCUITS WITH MULTIPLE
CIRCUIT LAYERS

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

AMENDMENT FILED WITH REQUEST FOR CONTINUED EXAMINATION

Dear Honorable Commissioner:

In response to the Final Office Action mailed on September 7, 2006, in connection with U.S. Patent Application No. 10/615,139, please consider the following amendments and remarks set forth below. **This Response is being timely filed within the shortened statutory time limit provided for such response so that no extension fee applies. If it is determined that there is a further fee associated with this Response, the United States Patent and Trademark Office is requested to consider this Response (with a petition if necessary) and charge the appropriate fee to Deposit Account No. 19-2814.**

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks begin on page 7 of this paper.